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**FACSIMILE TRANSMITTAL
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Date: May 12, 2004

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No. of Pages (including this page): 3

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To: Examiner S. B. Geyer
U.S. Patent and Trademark Office
Alexandria, VA 22313-1450

Group Art Unit: 2829

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Application No.: 10/719209
First Named Inventor: Theiss, Steven D
Title: Method of Making Transistors

Case No.: 57389US004

Attachments: Response to Election of Species Requirement

RECEIVED
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Patent
Case No.: 57389US004**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

First Named Inventor: THEISS, STEVEN D

Application No.: 10/719209

Group Art Unit: 2829

Filed: November 21, 2003

Examiner: S. B. Geyer

Title: METHOD OF MAKING TRANSISTORS

OFFICIAL**RESPONSE TO ELECTION OF SPECIES REQUIREMENT**Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**CERTIFICATE OF TRANSMISSION**

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I hereby certify that this correspondence is being facsimile transmitted to the U.S. Patent and Trademark Office on:

MAY 12 2004

Date

Signed by: Hylis H. Froelke

Dear Sir:

This is in response to the Office Action mailed May 4, 2004.

Remarks

Claims 1 – 23 are pending.

Species Election Requirement

Election of one from each of the following lettered groups was required under 35 U.S.C. Section 121:

A. Semiconductor device article

1. Semiconductor device article having only one semiconductor feature and also having a permanently affixed shadow mask.
2. Semiconductor device article having only one semiconductor feature and having a removable shadow mask.